# **BCA-107: LOGICAL ORGANIZATION OF COMPUTER-II**

### JANURARY-

UNIT - I

Sequential Logic: Characteristics, Flip-Flops, Clocked RS, D type, JK, T type and Master-Slave flip-flops. State table, state diagram and state equations. Flip-flop excitation tables.

#### **FEBRUARY-**

UNIT - II

Sequential Circuits: Designing registers – Serial Input Serial Output (SISO), Serial Input Parallel Output (SIPO), Parallel Input Serial Output (PISO), Parallel Input Parallel Output (PIPO) and shift registers. Designing counters – Asynchronous and Synchronous Binary Counters, Modulo-N Counters and Up-Down Counters.

## MARCH-

UNIT - III

Memory & I/O Devices: Memory Parameters, Semiconductor RAM, ROM, Magnetic and Optical Storage devices, Flash memory, I/O Devices and their controllers.

#### **APRIL**

UNIT - IV

Instruction Design & I/O Organization: Machine instruction, Instruction set selection, Instruction cycle, Instruction Format and Addressing Modes. I/O Interface, Interrupt structure, Program-controlled, Interrupt-controlled & DMA transfer, I/O Channels, IOP.